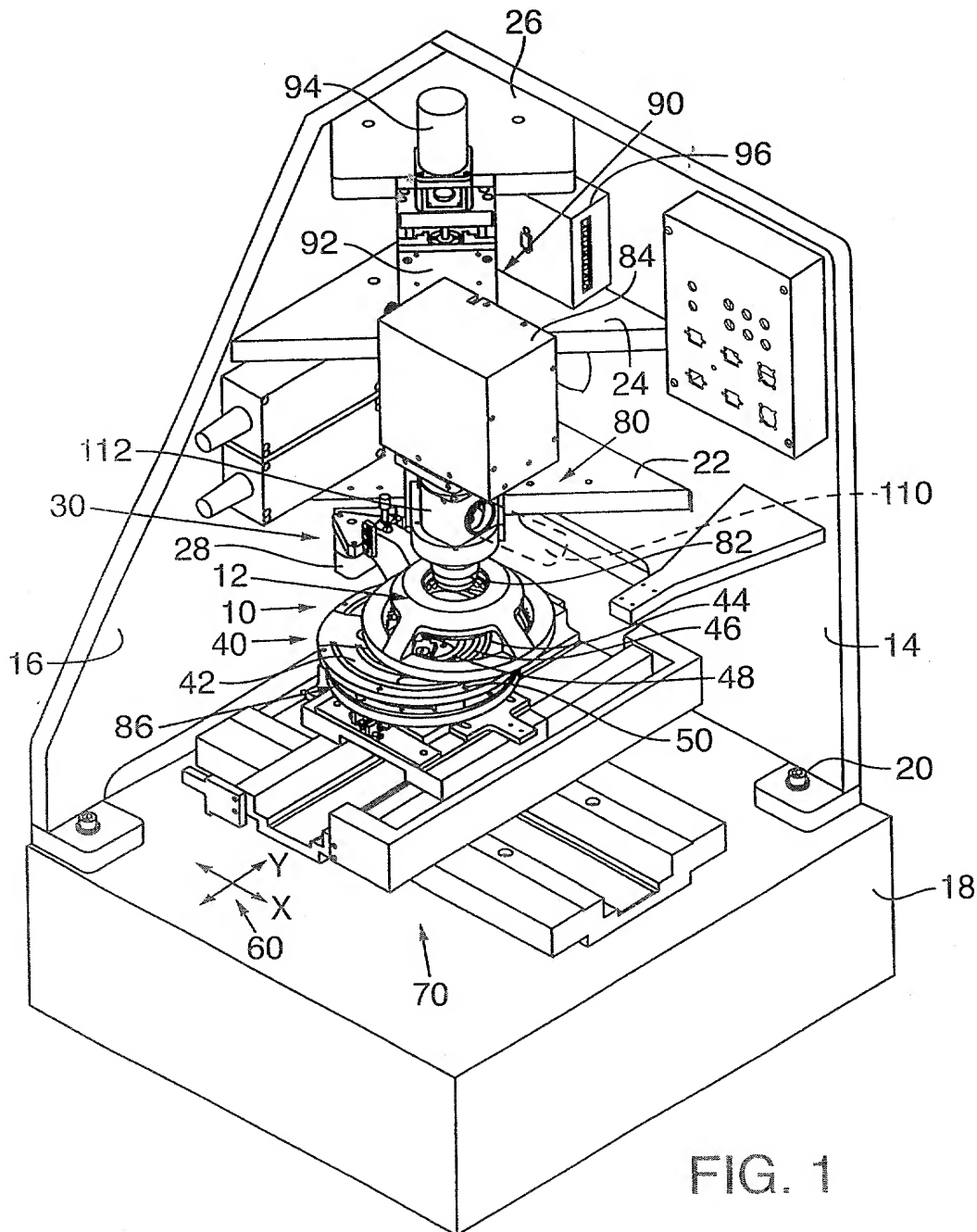


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10047980.011102



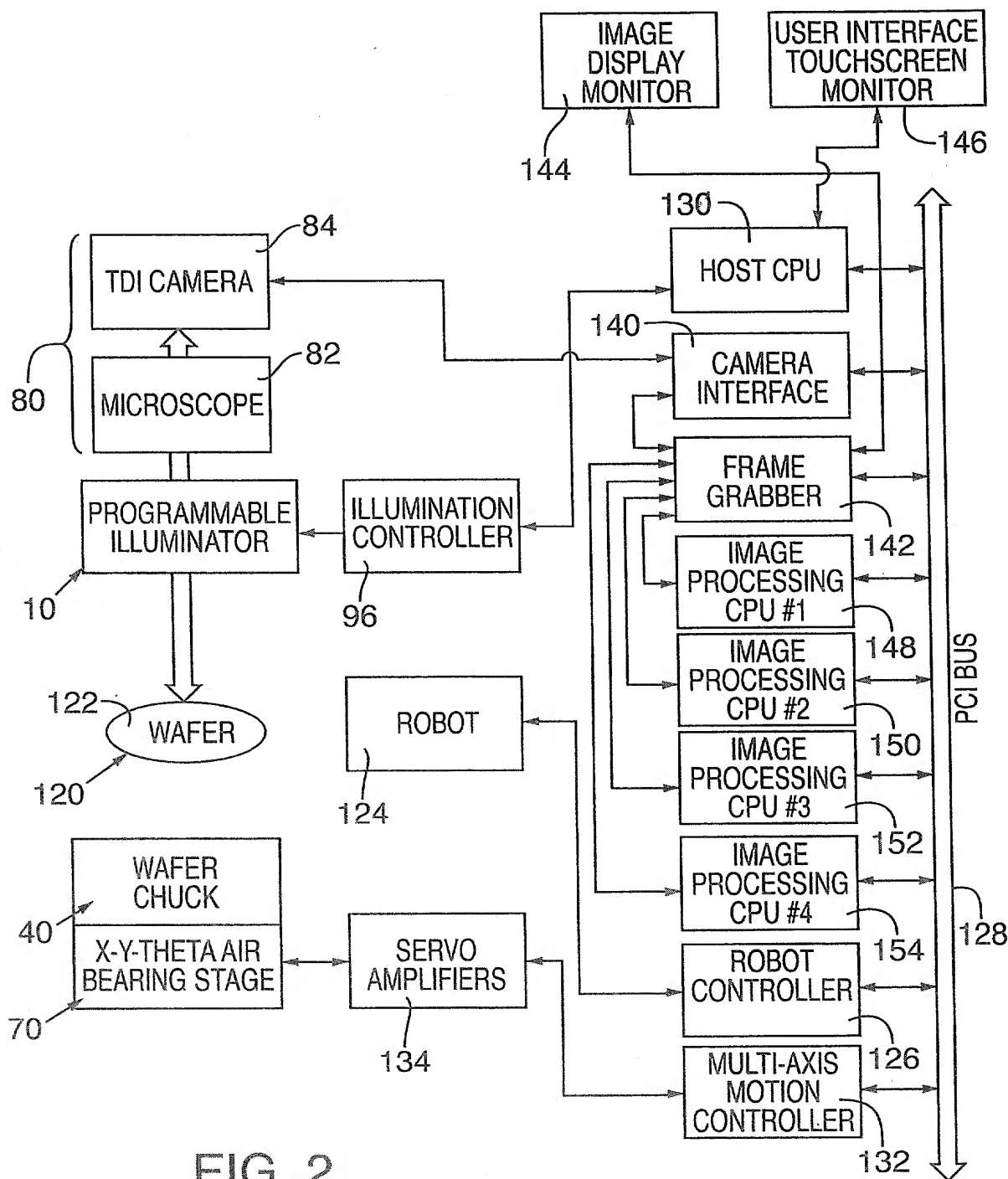
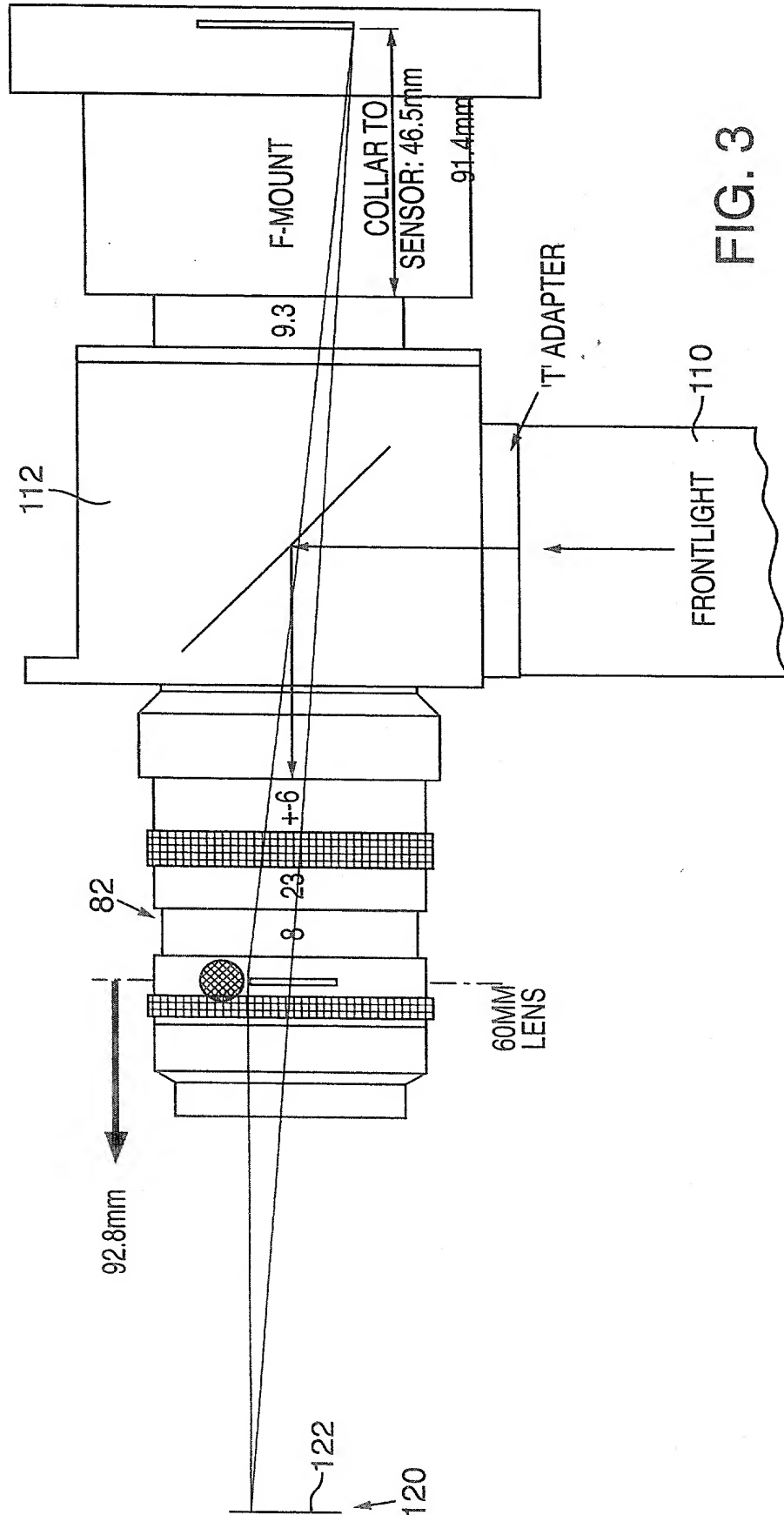


FIG. 2

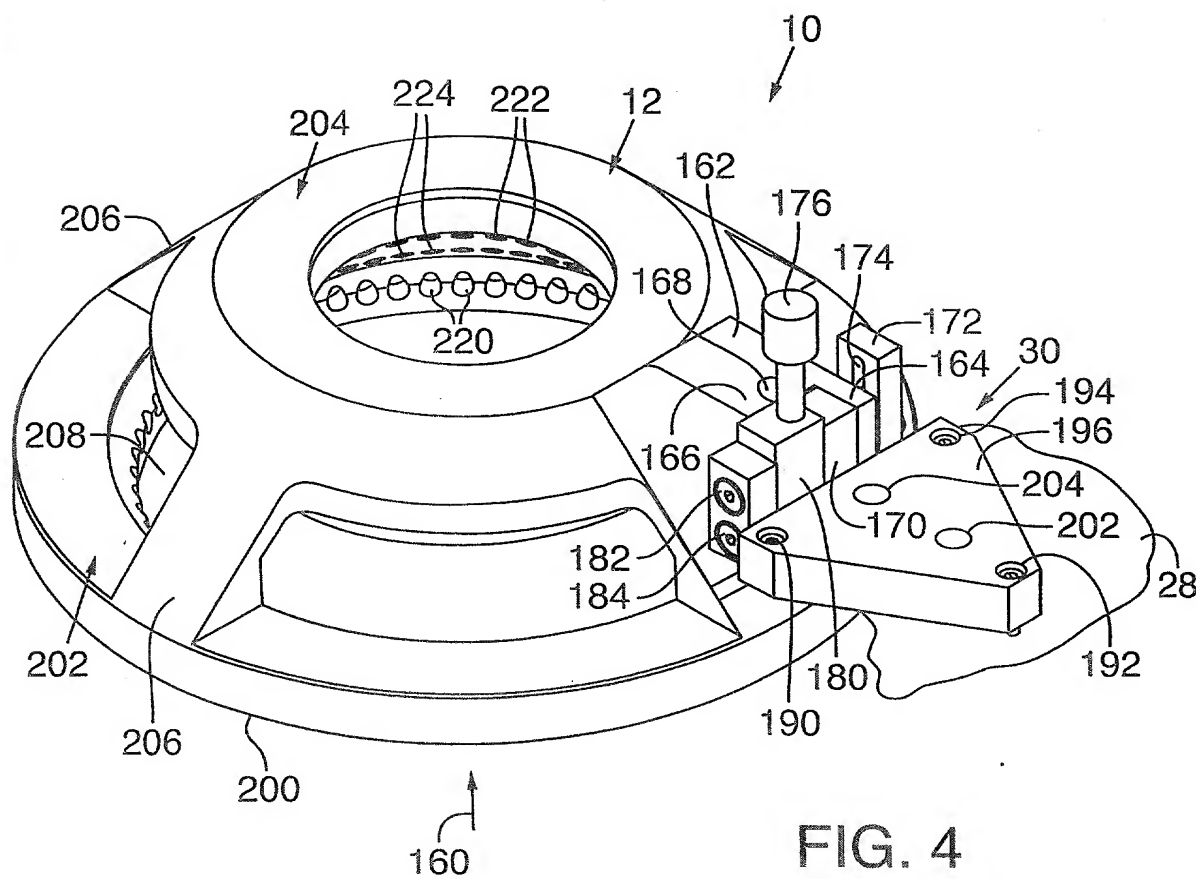
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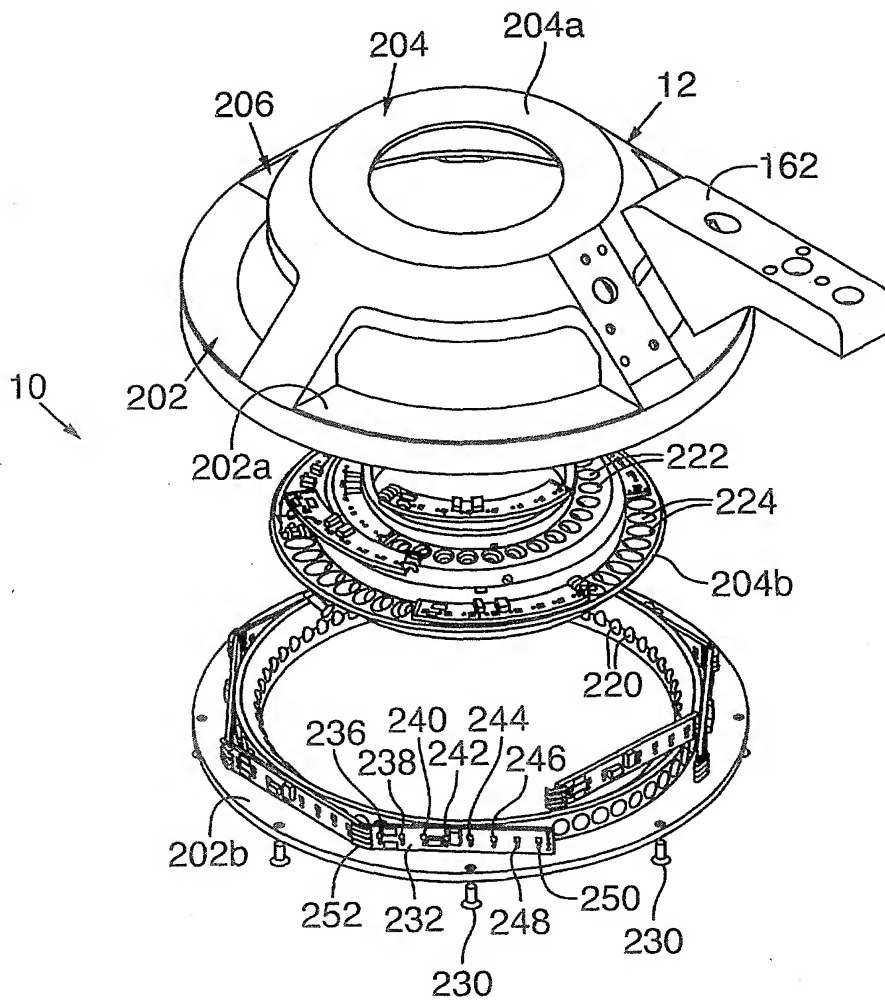


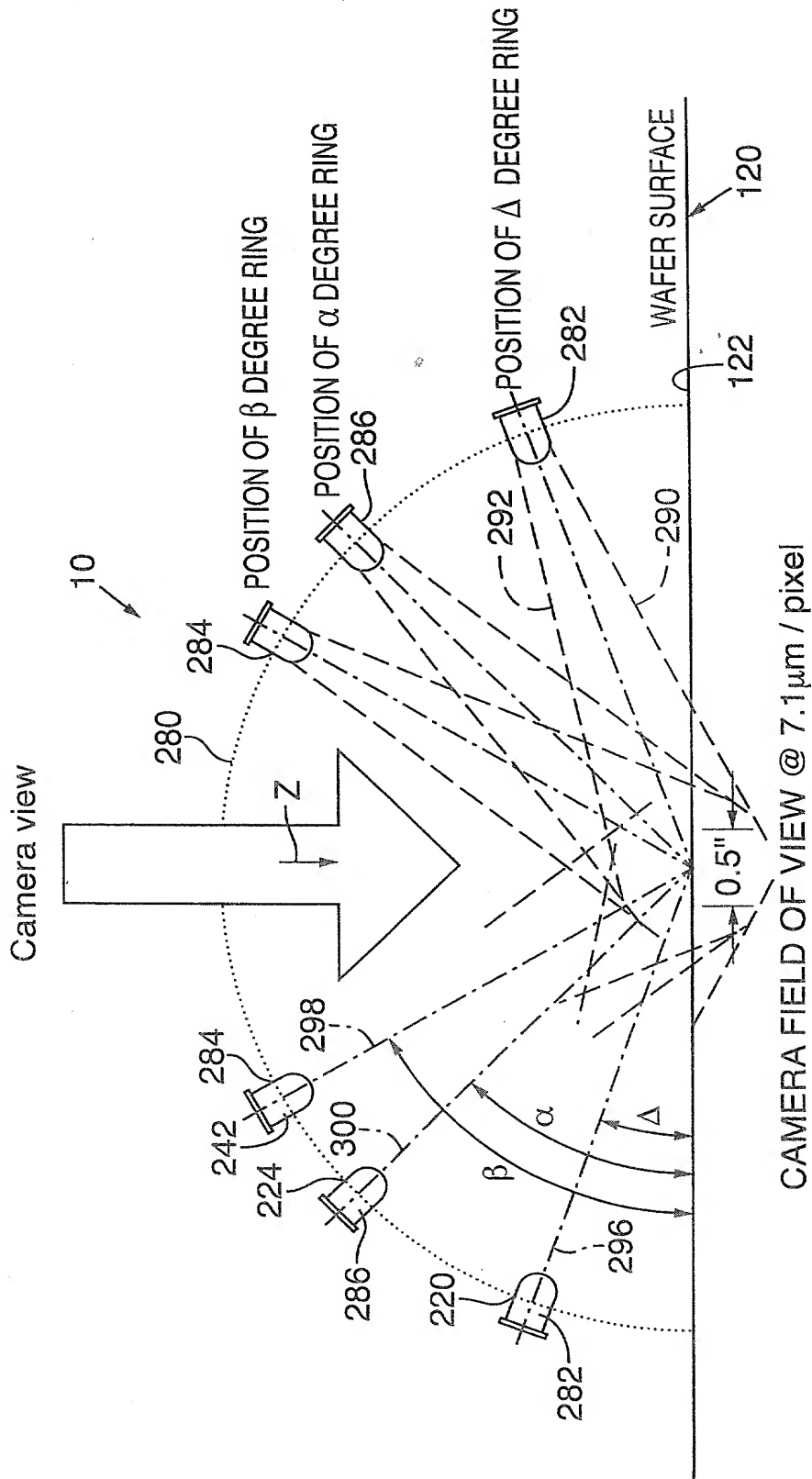
FIG. 5

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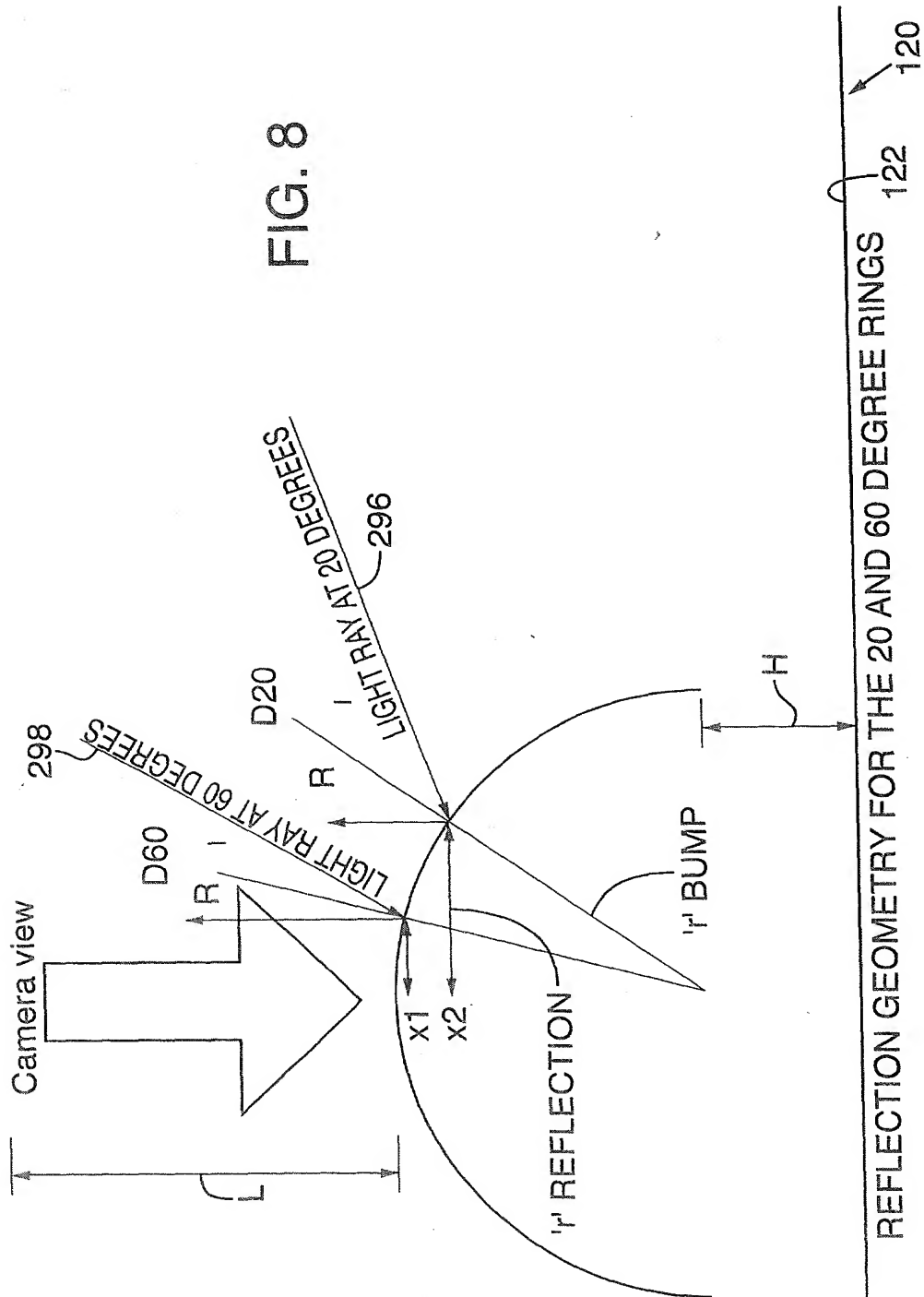
SIDE VIEW OF RING/WAFER GEOMETRY

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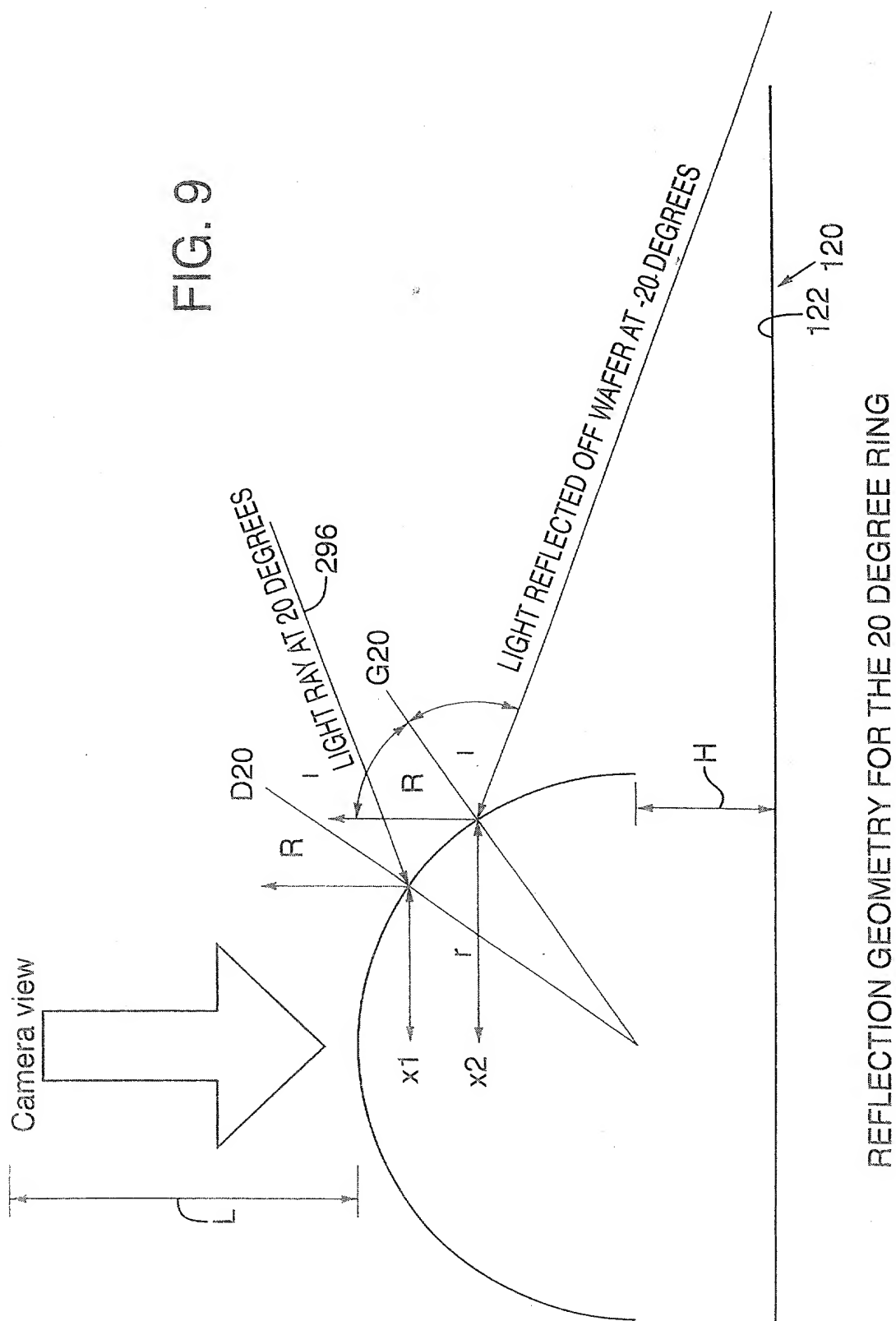
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SEMICONDUCTOR WAFER BUMP

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REFLECTION GEOMETRY FOR THE 60 DEGREE RING

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GENERATING VALUES ...

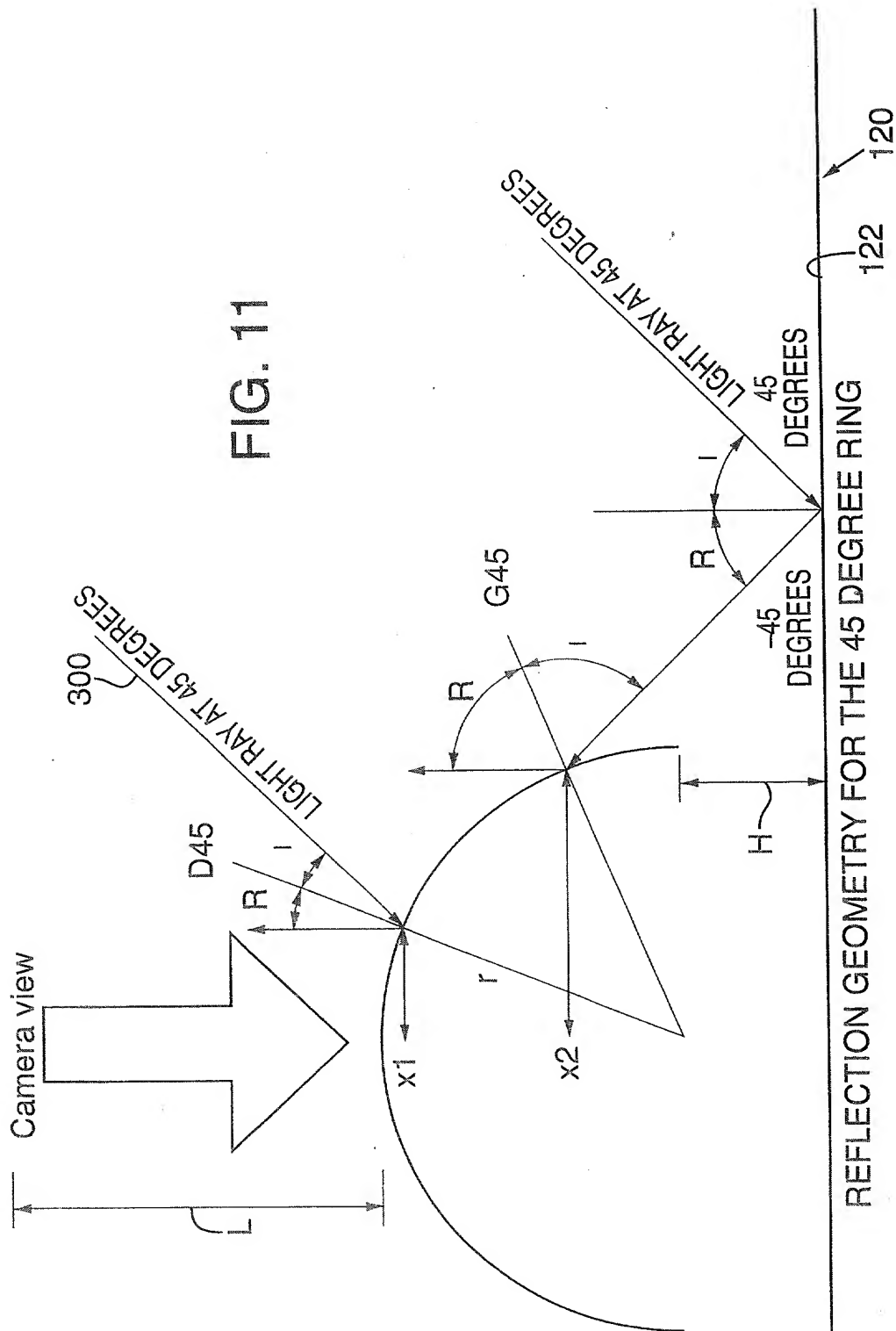
GENERATING VALUES . . .

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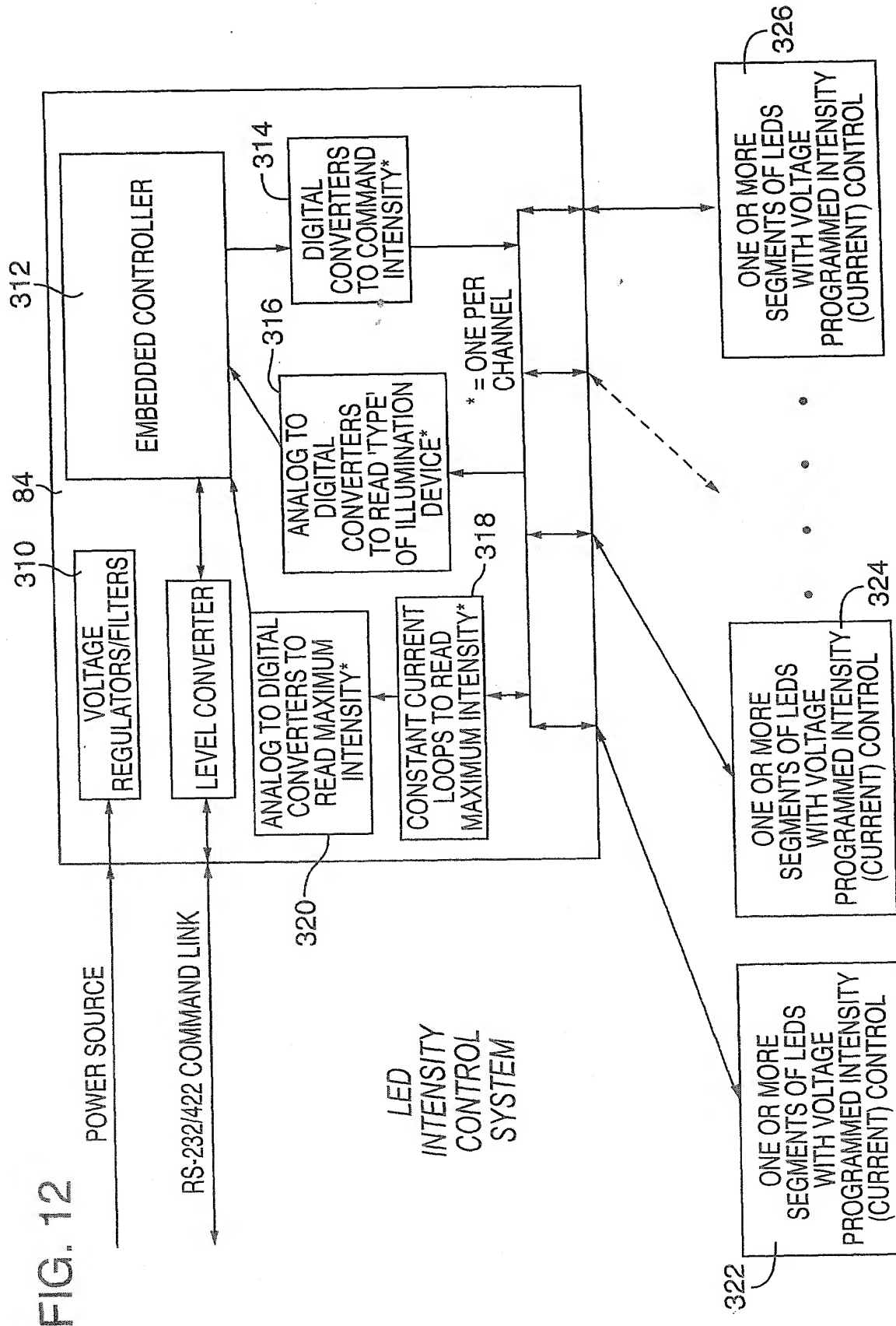
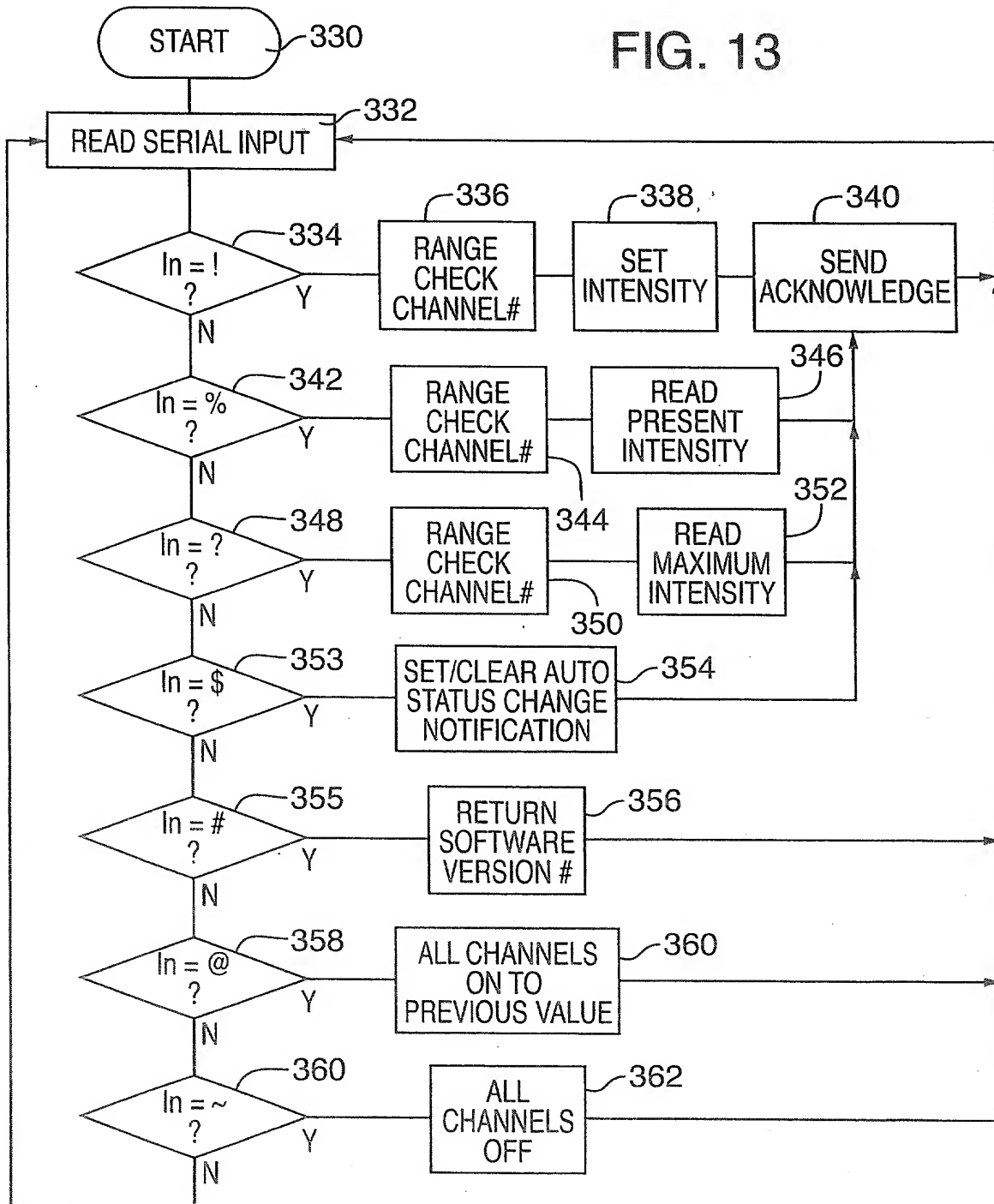


FIG. 13



EMBEDDED CONTROLLER PROGRAM FLOW

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SEMICONDUCTOR WAFER COMPS
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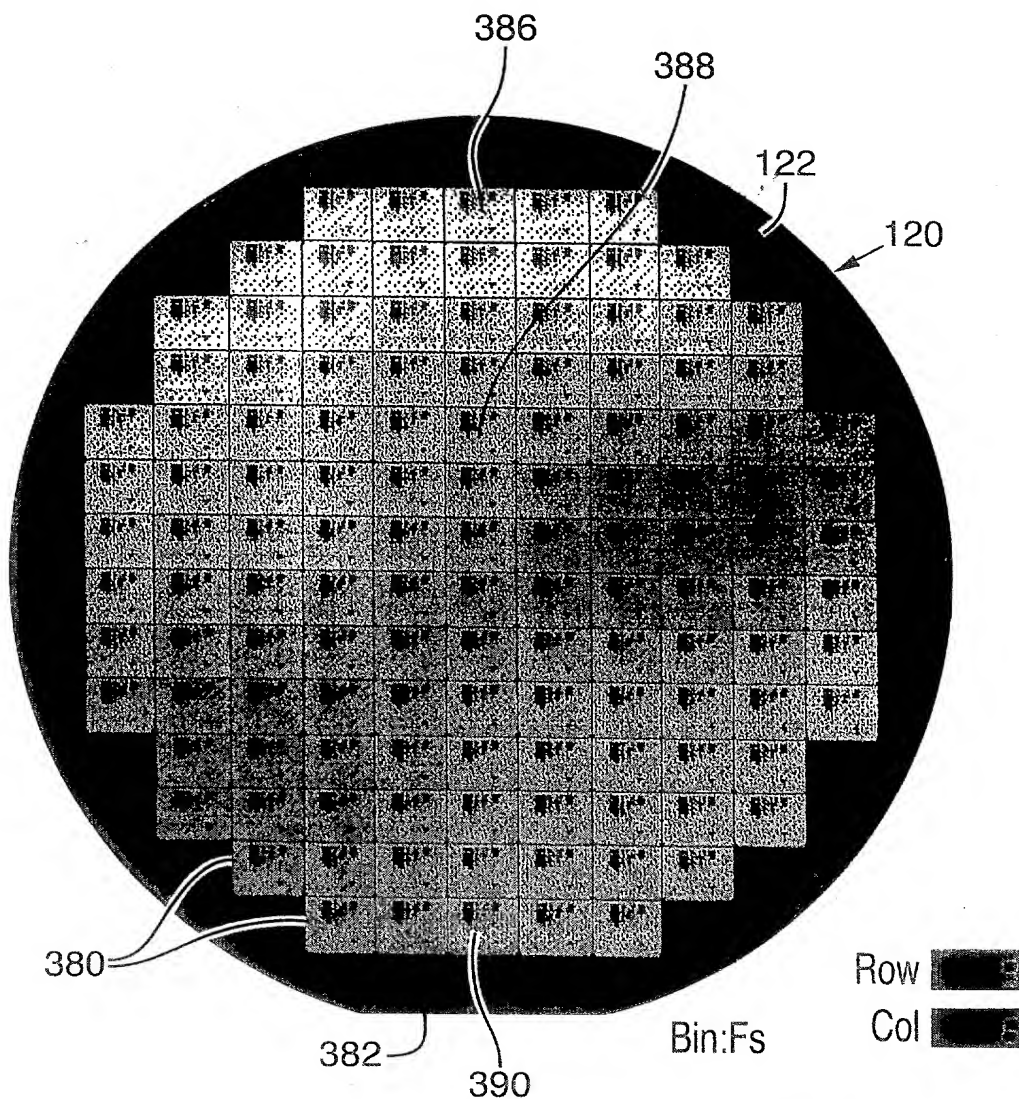
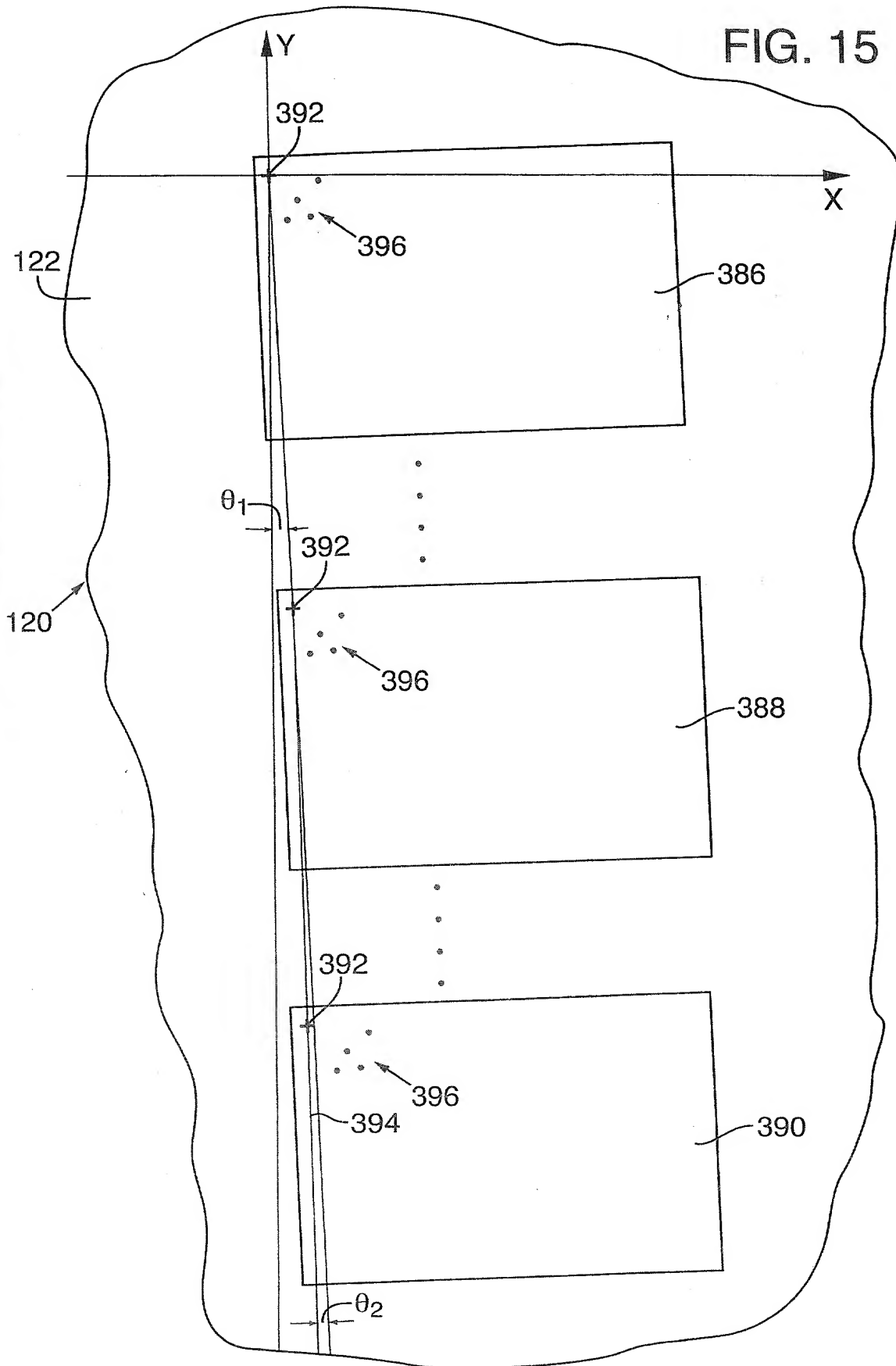


FIG. 14

FIG. 15



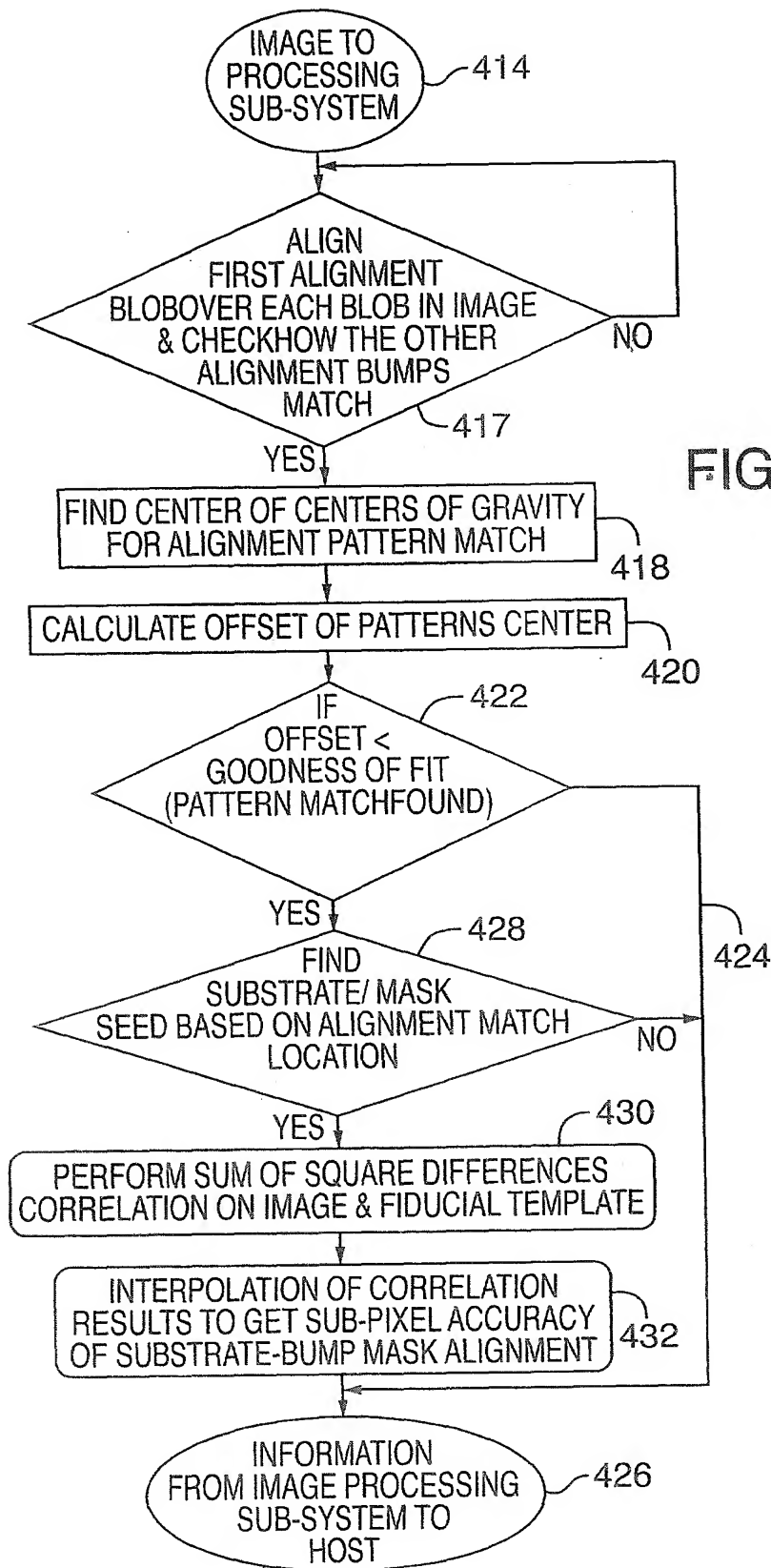
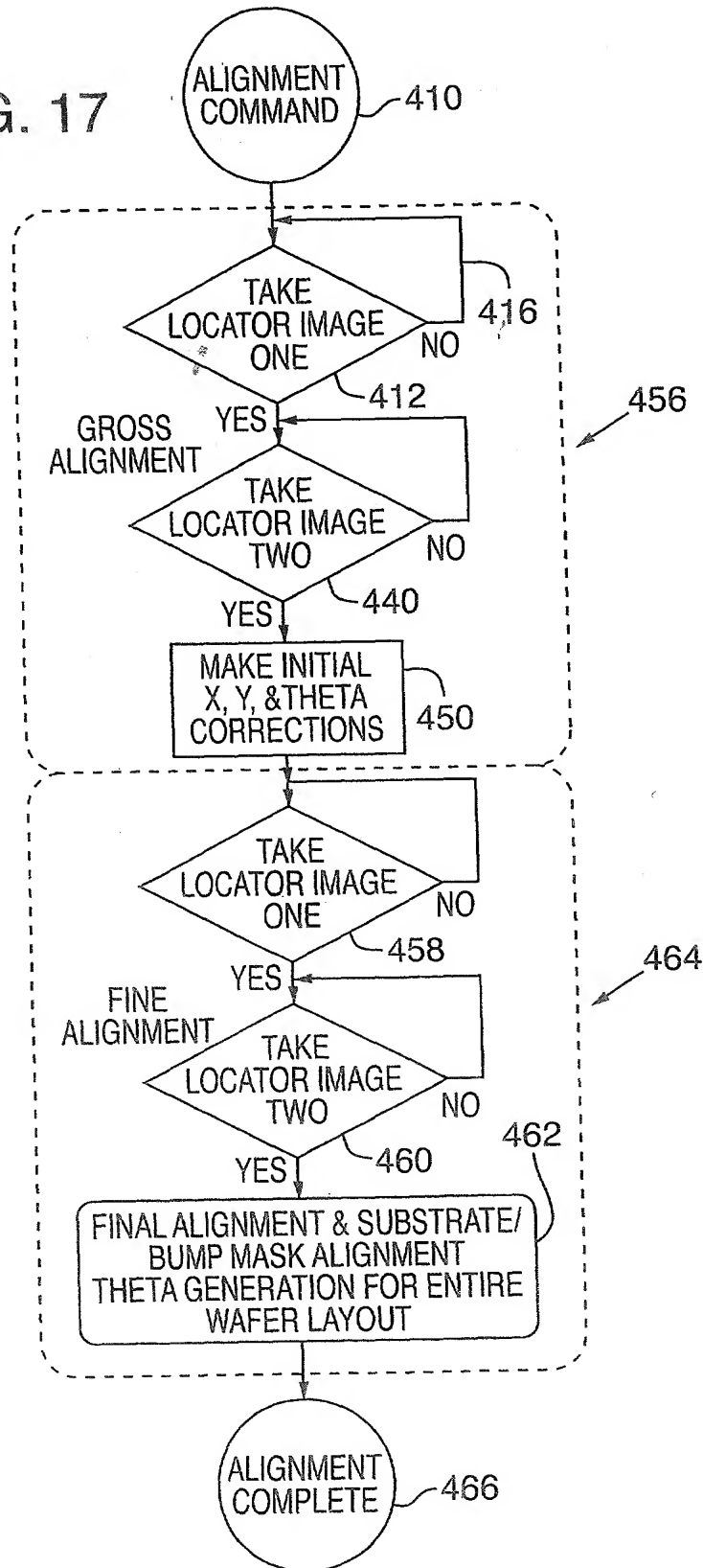


FIG. 17



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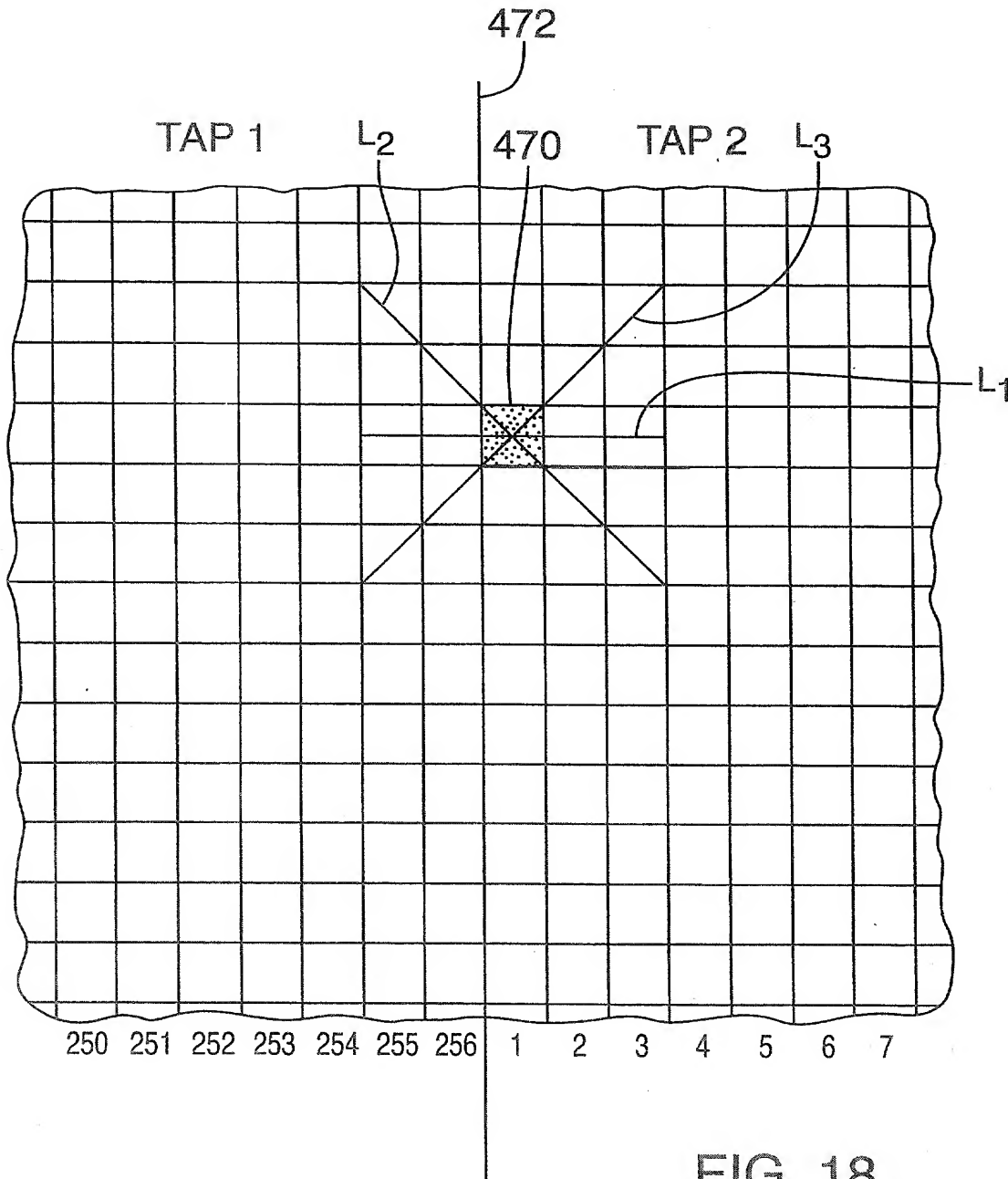
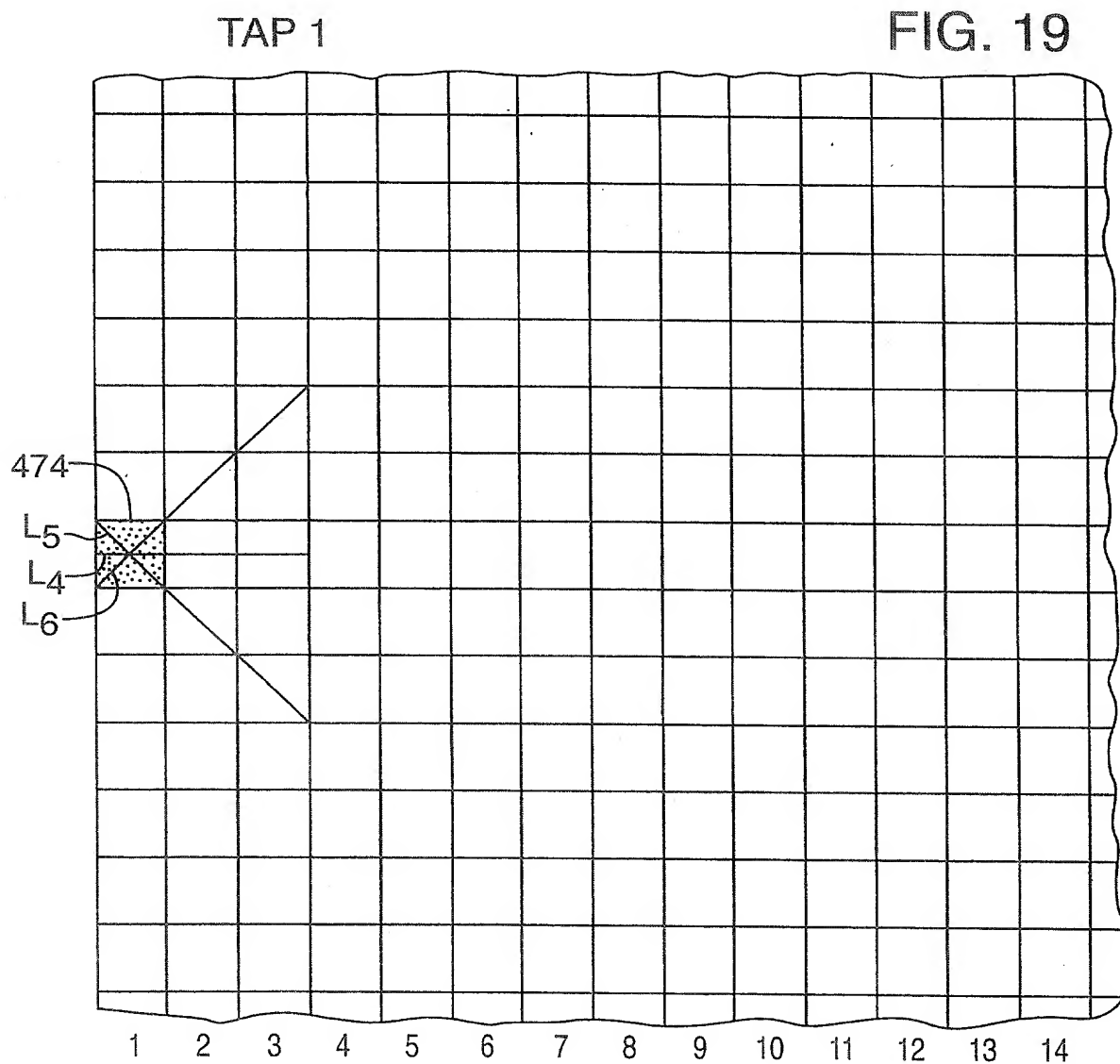


FIG. 18

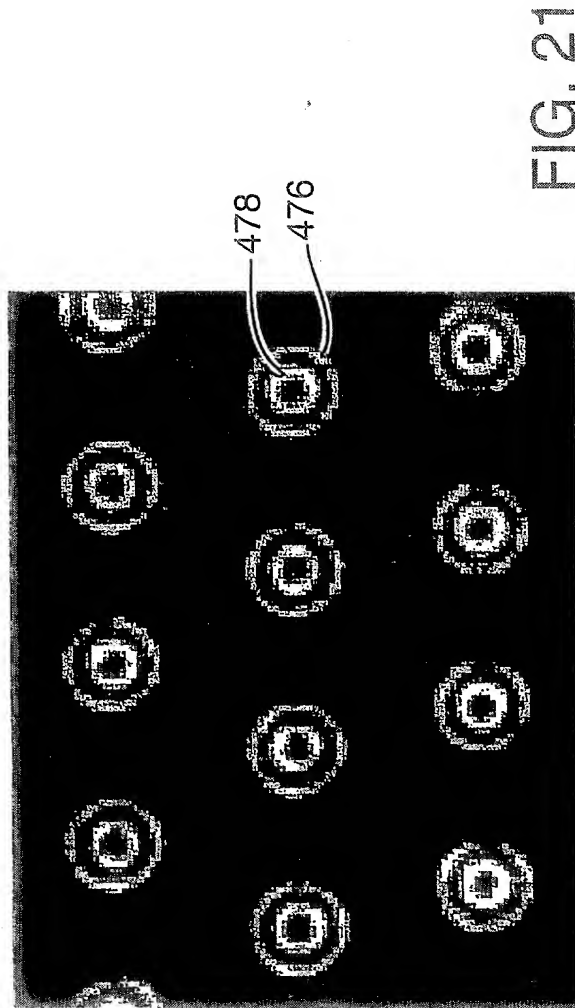
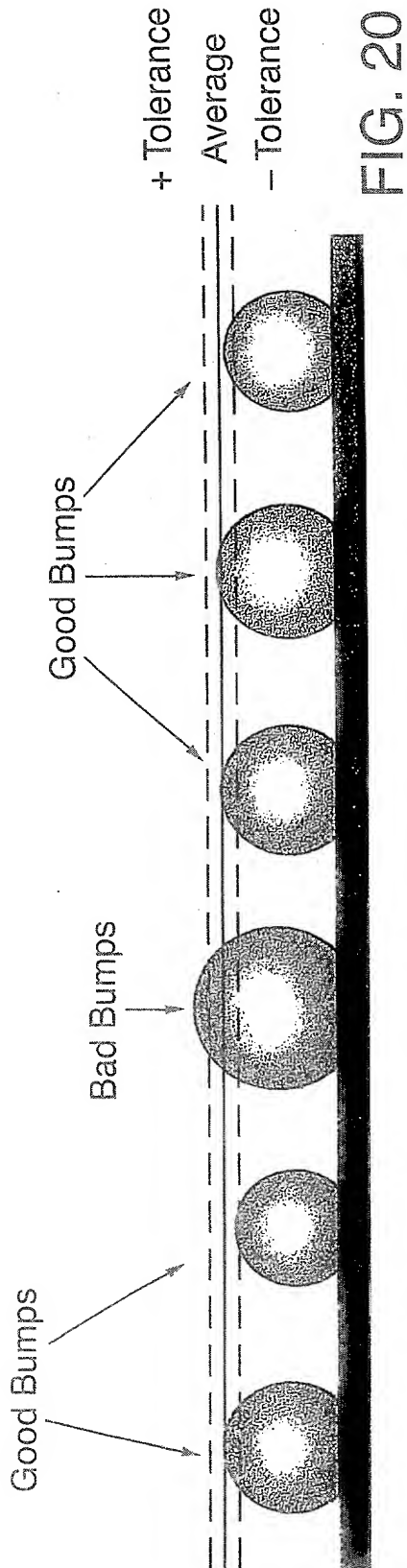
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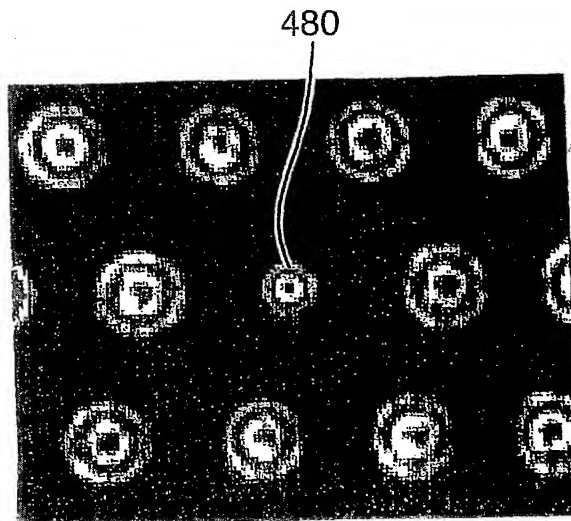


FIG. 22

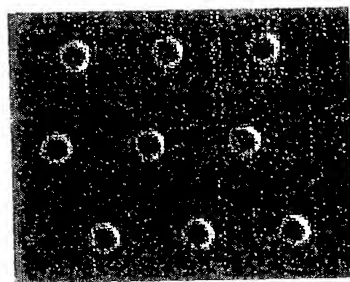


FIG. 23

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SEMICONDUCTOR WAFER BONES
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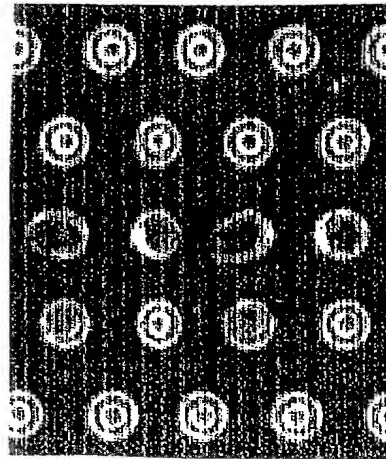
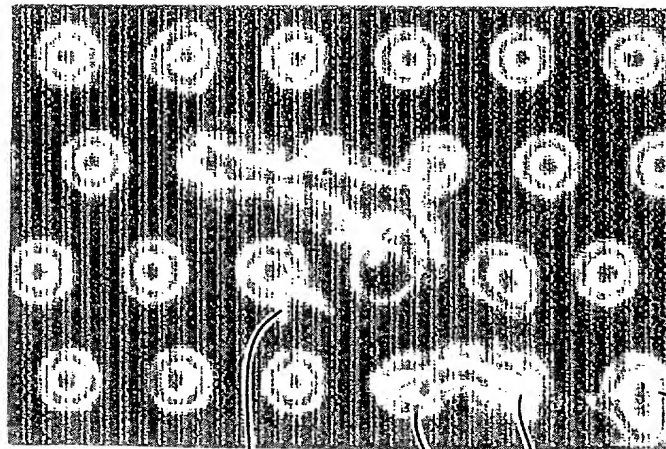


FIG. 24



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FIG. 25